



S/N 09/893,023

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Frank O'Mahony et al.

Examiner: Ahmed N. Sefer

Serial No.: 09/893,023

Group Art Unit: 2826

Filed: June 27, 2001

Docket: 884.405US1

Title: LOW LOSS INTERCONNECT STRUCTURE FOR USE IN
MICROELECTRONIC CIRCUITS#4/Election
Amot
4
10/16/02
✓RESPONSE TO RESTRICTION REQUIREMENTCommissioner for Patents
Washington, D.C. 20231

In response to the Restriction Requirement mailed September 13, 2002, Applicants identify the following species:

Species I: Figs. 3 and 4; claims 1-12

Species II: Fig. 5, claims 19-25

Species III: Fig 8, claims 13-18

Applicant elects without traverse, Species III (claims 13-18). Applicant cancels remaining claims 1-12 and 19-25 without prejudice or disclaimer, and reserves the right to reintroduce them in one or more divisional applications at a later date.

The Examiner is invited to contact the below signed attorney if there are any questions regarding this Response or if prosecution of this application may be assisted thereby.

Respectfully submitted,
FRANK O'MAHONY ET AL.
By their Representatives,

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Date 10-1-02 By Dana B. LeMoine
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this 1 day of September, 2002.

Jane E. Brockschink
NameJane E. Brockschink
SignatureRECEIVED
OCT 10 2002
TECHNOLOGY CENTER 2800